

USSN: 10/761,724

Group No. 2841

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**AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions and listings of claims in the application:

**LISTING OF CLAIMS**

1. (currently amended) A ball grid array package comprising:
  - a chip including a substrate with a bottom surface;
  - a plurality of solder bumps projecting outwardly from said bottom surface of said substrate; and
  - an electromagnetic shield including a housing that defines an inner space which receives said chip and said solder bumps therein, and a bottom opening for access into said inner space;
  - wherein said solder bumps project outwardly of said inner space through said bottom opening in said housing;
  - wherein said housing of said electromagnetic shield has a peripheral wall that surrounds said chip and said solder bumps; and
  - wherein said peripheral wall is formed with at least one side opening, said substrate having a corresponding tab projecting outwardly therefrom and extending transversely of said peripheral wall through said side opening.
2. (original) The ball grid array package of claim 1, wherein said housing of said electromagnetic shield is connected directly to said substrate of said chip.
3. (cancelled)
4. (cancelled)
5. (currently amended) The ball grid array package of claim [[4]] 2, wherein said peripheral wall has a bottom end, said side opening extending to said bottom end.

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6. (currently amended) The ball grid array package of claim [[3]] 2, wherein said peripheral wall has a bottom end, two first recesses extending to said bottom end, and two vertical slits disposed between said first recesses to form a flexible part therebetween.

7. (original) The ball grid array package of claim 6, wherein said flexible part is formed with a stop that projects into said inner space in said housing, said substrate being supported on said stop.

8. (currently amended) An electronic device comprising:

a printed circuit board;

a chip including a substrate with opposite top and bottom surfaces, and a semiconductor die mounted on said top surface of said substrate;

a plurality of solder bumps projecting outwardly from said bottom surface of said substrate and connected directly and electrically to said printed circuit board; and

an electromagnetic shield including a housing that defines an inner space which receives said chip and said solder bumps therein, and a bottom opening for access into said inner space, said housing having a bottom end that defines said bottom opening and that is connected directly to said printed circuit board;

wherein said solder bumps project outwardly of said inner space through said bottom opening in said housing;

wherein said housing of said electromagnetic shield has a peripheral wall that surrounds said chip and said solder bumps; and

wherein said peripheral wall is formed with at least one side opening, said substrate having a corresponding tab projecting outwardly therefrom and extending transversely of said peripheral wall through said side opening.

9. (original) The electronic device of claim 8, wherein said housing of said electromagnetic shield is connected directly to said substrate of said chip.